A walk-in-interview will be held in Seminar Hall, Department of Electronics and Telecommunication Engineering, (ETCE), Jadavpur University, Kolkata-700032, on 24th October, 2011 at 12.00 noon for recruitment of one JRF post for the project entitled “Design and simulation of logic circuits with hybrid architectures of Single electron transistors and conventional MOS devices”, under CSIR sponsored research scheme of Prof. Subir kumar Sarkar (Principal Investigator), Department of Electronics and Telecommunication Engineering, Jadavpur University as per the details below:

Qualification and age: MSc/BE/BTech or equivalent degree, with 55% marks and the upper age limit shall be 28 years.

Stipend: Rs.12000/- p.m. +15% HRA

Tenure: 3 years or till the completion of the project.

Interested candidates may appear in the interview directly in person with all original testimonials, two photographs and an application on prescribed from (priced Rs. 50/-) to be obtained from the Information Section of the University.

No TA/DA is admissible.

Note: Candidate may register for PhD during the project period.

Registrar